

### NEW PRODUCT

#### FUSIO RT DEVELOPMENT KIT

##### 3DMC0753-1 CN EVAL

The **3DMC0753-1 CN EVAL** is a Development Kit based on the FUSIO RT P/N = 3DMC0753-1. It allows the users to get started with FUSIO RT module and its design software tools.

The development kit is a quick way to prototype different applications, the FPGA can be easily programmed via JTAG, SpaceWire, or other interfaces.

#### **DEVELOPMENT KIT DELIVERABLES:**

**Development Board** with assembled FUSIO RT P/N = 3DMC0753-1

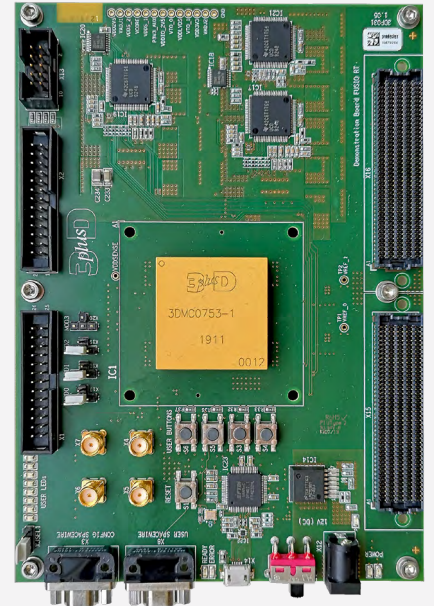
#### **Cables and power supply**

- Universal 12 V Power supply and adapter plugs (USA and EU)
- USB cable for PC connection

#### **Documentation:**

- FUSIO RT Datasheet : 3DDS-0753
- Development Kit User Guide: 3300-9470
- Development Board schematics: EL32-1021

**Software:** FUSIO RT loader (GUI)



#### FUSIO RT

##### 3DMC0753-1

The FUSIO RT Computer Core product family integrates a reprogrammable Space grade FPGA and a highly reliable configuration memory in a single miniaturized package. The FPGA core is the space grade NG-Medium NX1H35AS SRAM based FPGA.

The configuration memory of the FUSIO RT is based on a Triplicated Modular Redundancy NOR Flash. Thanks to its triplicated NOR Flash and its power switch, it offers SEU immunity and high TID performance.

This All-in-One solution brings additional key benefits:

- Reprogrammable FPGA,
- High performances and capabilities (speed, power consumption, macro blocks, I/Os...),
- Significant reduction of the board area,
- Product family approach - modularity, upgradable computer core,
- ITAR free.

The FUSIO RT Computer Core module is also available with NAND Flash memory for mass data storage and SDRAM memory for data processing, bringing higher capability to the FPGA based system.

### NEXT EXHIBITION